| L Number Hits Search Text  19620 microcontroller  998 microcontroller and mux 0 microcontroller and mux and internal and uspar 2003/09/2 external and peripharals  1355 microcontroller and mux and internal and uspar 2003/09/2 external and peripheral  10 (microcontroller and mux and internal and external and peripharals) and structurable  10 (microcontroller and mux and internal and external and peripharals) and structurable  10 (microcontroller and mux and internal and external and peripharals) and interligent and interface  10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface  10 (microcontroller and mux and internal and external and peripheral) and structurable  10 (microcontroller and mux and internal and external and peripheral) and structurable  11 (microcontroller and mux and internal and external and peripheral) and interligent and interface and core and interface and core and internal and external and peripheral) and intelligent and interface and core and (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  11 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  12 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  13 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  13 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)         | 6 17:12<br>6 17:02<br>6 17:03<br>6 17:10<br>6 17:05<br>6 17:04<br>6 17:05<br>6 17:04<br>6 17:05<br>6 17:05<br>6 17:05<br>6 17:05 |
|--|--|
| - 19620 microcontroller microcontroller and mux microcontroller and mux us and internal and microcontroller and mux and internal and microcontroller and mix and internal and microcontroller and mix and internal and microcontroller and mix and internal and mix | 6 17:12<br>6 17:02<br>6 17:03<br>6 17:10<br>6 17:05<br>6 17:04<br>6 17:05<br>6 17:04<br>6 17:05<br>6 17:05<br>6 17:05<br>6 17:05 |
| - 998 microcontroller and mux microcontroller and mux and internal and external and peripharals microcontroller and mux and internal and external and peripharals and structurable (microcontroller and mux and internal and external and peripharals) and structurable (microcontroller and mux and internal and external and peripharals) and structural uspat 2003/09/2 200 | 6 17:02<br>6 17:03<br>6 17:10<br>6 17:05<br>6 17:04<br>6 17:05<br>6 17:04<br>6 17:05<br>6 17:05<br>6 17:05<br>6 17:06            |
| microcontroller and mux and internal and external and peripharals microcontroller and mux and internal and external and peripheral  (microcontroller and mux and internal and external and peripharals) and structurable (microcontroller and mux and internal and external and peripharals) and structural.  (microcontroller and mux and internal and external and peripharals) and internal and external and peripharals) and internal and external and peripharals) and interligent and interface  (microcontroller and mux and internal and external and peripheral) and interligent and interface  (microcontroller and mux and internal and external and peripheral) and internal and external and peripheral) and structurable  (microcontroller and mux and internal and external and peripheral) and structurable (microcontroller and mux and internal and external and peripheral) and interligent core" and interface  (microcontroller and mux and internal and external and peripheral) and interligent and interface and core  (microcontroller and mux and internal and external and peripheral) and interligent and interface and core  (microcontroller and mux and internal and external and peripheral) and interligent and interface and core  (microcontroller and mux and internal and external and external and peripheral) and interligent and interface and core  (microcontroller and mux and internal and external and external and external and interface and core  (microcontroller and mux and internal and external and external and external and interface and core  (microcontroller and mux and internal and external and external and external and interface and core  (microcontroller and mux and internal and external and external and external and internal and internal and external and external and external and internal and internal and external and external and external and internal and internal and external and external and external and external and internal and internal and external  | 6 17:03<br>6 17:10<br>6 17:05<br>6 17:04<br>6 17:05<br>6 17:05<br>6 17:05<br>6 17:05<br>6 17:06                                  |
| external and peripharals microcontroller and mux and internal and external and peripheral  (microcontroller and mux and internal and external and peripharals) and structurable (microcontroller and mux and internal and external and peripharals) and structural.  (microcontroller and mux and internal and external and peripharals) and internal and external and peripharals) and interligent and interface (microcontroller and mux and internal and external and peripheral) and interligent and interface (microcontroller and mux and internal and external and peripheral) and internal and external and peripheral) and structurable (microcontroller and mux and internal and external and peripheral) and structurable (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface (microcontroller and mux and internal and external and peripheral) and interligent core" and interface (microcontroller and mux and internal and external and peripheral) and intelligent core" and interface (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data) ((microcontroller and mux and internal and interface and core and (process near data) ((microcontroller and mux and internal and intern | 6 17:10<br>6 17:05<br>6 17:04<br>6 17:05<br>6 17:04<br>6 17:05<br>6 17:05<br>6 17:06   |
| S55   microcontroller and mux and internal and external and peripheral   USPAT   2003/09/2   | 6 17:05<br>6 17:04<br>6 17:05<br>6 17:05<br>6 17:05<br>6 17:05<br>6 17:06  |
| external and peripheral (microcontroller and mux and internal and external and peripharals) and structurable (microcontroller and mux and internal and external and peripharals) and structural (microcontroller and mux and internal and external and peripharals) and internal and external and peripharals) and internal and external and peripharals) and internal and external and peripheral) and interligent and interface  83 (microcontroller and mux and internal and external and peripheral) and intelligent and interface (microcontroller and mux and internal and external and peripheral) and structurable (microcontroller and mux and internal and external and peripheral) and structurable (microcontroller and mux and internal and external and peripheral) and interligent core" and interface (microcontroller and mux and internal and external and peripheral) and interligent and interface and core (microcontroller and mux and internal and external and peripheral) and interligent and interface and core  10 (microcontroller and mux and internal and external and peripheral) and interligent and interface and core (microcontroller and mux and internal and external and peripheral) and interligent and interface and core  10 (microcontroller and mux and internal and external and peripheral) and interligent and interface and core (microcontroller and mux and internal and external and peripheral) and internal and uspart 2003/09/2  10 (microcontroller and mux and internal and external and peripheral) and internal and uspart 2003/09/2  | 6 17:05<br>6 17:04<br>6 17:05<br>6 17:05<br>6 17:05<br>6 17:05<br>6 17:06  |
| Commitment   Com   | 6 17:04<br>6 17:05<br>6 17:04<br>6 17:05<br>6 17:05<br>6 17:06   |
| external and peripharals) and structurable (microcontroller and mux and internal and external and peripharals) and structural  or (microcontroller and mux and internal and external and peripharals) and interligent and interface  or (microcontroller and mux and internal and external and peripheral) and interligent and interface  or (microcontroller and mux and internal and external and peripheral) and interligent and interface  or (microcontroller and mux and internal and external and peripheral) and structurable  or (microcontroller and mux and internal and external and peripheral) and structurable  or (microcontroller and mux and internal and external and peripheral) and interligent core and interface  or (microcontroller and mux and internal and external and peripheral) and interligent and interface and core  or (microcontroller and mux and internal and external and peripheral) and interligent and interface and core  or (microcontroller and mux and internal and external and peripheral) and interligent and interface and core  or (microcontroller and mux and internal and external and peripheral) and interligent and interface and core and (process near data)  or (microcontroller and mux and internal and external and peripheral) and internal and or interface and core and (process near data)  or (microcontroller and mux and internal and external and peripheral) and internal and uspar 2003/09/20  or (microcontroller and mux and internal and external and peripheral) and internal and uspar 2003/09/20  or (microcontroller and mux and internal and external and peripheral) and internal and uspar 2003/09/20  or (microcontroller and mux and internal and external and peripheral) and internal and uspar 2003/09/20  or (microcontroller and mux and internal and external and external and peripheral) and internal and uspar 2003/09/20  or (microcontroller and mux and internal and external  | 6 17:04<br>6 17:05<br>6 17:04<br>6 17:05<br>6 17:05<br>6 17:06   |
| 0  | 6 17:05<br>6 17:04<br>6 17:05<br>6 17:05<br>6 17:06<br>6 17:06   |
| external and peripharals) and structural (microcontroller and mux and internal and external and peripheral) and intelligent and interface  83 (microcontroller and mux and internal and external and peripheral) and intelligent and interface  (microcontroller and mux and internal and external and peripheral) and intelligent and interface  (microcontroller and mux and internal and external and peripheral) and structurable  (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface  (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core  (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  ((microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  ((microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  ((microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)   | 6 17:05<br>6 17:04<br>6 17:05<br>6 17:05<br>6 17:06<br>6 17:06   |
| - 0 (microcontroller and mux and internal and external and peripharals) and intelligent and interface - 83 (microcontroller and mux and internal and external and peripheral) and intelligent and interface - 83 (microcontroller and mux and internal and external and peripheral) and intelligent and interface - 0 (microcontroller and mux and internal and external and peripheral) and structurable - 0 (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface - 44 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core - 10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data) - 7 ((microcontroller and mux and internal and USPAT 2003/09/2)  | 6 17:04<br>6 17:05<br>6 17:05<br>6 17:06<br>6 17:06  |
| external and peripharals) and intelligent and interface  (microcontroller and mux and internal and external and peripheral) and intelligent and interface  (microcontroller and mux and internal and external and peripheral) and intelligent and interface  (microcontroller and mux and internal and external and peripheral) and structurable (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface  (microcontroller and mux and internal and external and peripheral) and intelligent core" and interface  (microcontroller and mux and internal and interface and core (microcontroller and mux and internal and interface and core and (process near data)  ((microcontroller and mux and internal and interface and core and (process near data)  ((microcontroller and mux and internal and interface and core and (process near data)  ((microcontroller and mux and internal and interface and core and (process near data)  ((microcontroller and mux and internal and interface and core and (process near data)  ((microcontroller and mux and internal and interface and core and (process near data)   | 6 17:04<br>6 17:05<br>6 17:05<br>6 17:06<br>6 17:06  |
| and interface (microcontroller and mux and internal and external and peripheral) and intelligent and interface (microcontroller and mux and internal and external and peripheral) and intelligent and interface  (microcontroller and mux and internal and external and peripheral) and structurable (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface  44 (microcontroller and mux and internal and external and peripheral) and internal and interface and core  10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  7 ((microcontroller and mux and internal and vexternal and peripheral) and intelligent and interface and core and (process near data)  7 ((microcontroller and mux and internal and vexternal and vexternal and peripheral) and intelligent and interface and core and (process near data)  7 ((microcontroller and mux and internal and vexternal  | 6 17:05<br>6 17:05<br>6 17:06<br>6 17:06   |
| - 83 (microcontroller and mux and internal and external and peripheral) and intelligent and interface - 83 (microcontroller and mux and internal and external and peripheral) and intelligent and interface - 0 (microcontroller and mux and internal and external and peripheral) and structurable - 0 (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface - 44 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core - 10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data) - 7 ((microcontroller and mux and internal and USPAT 2003/09/2)  | 6 17:05<br>6 17:05<br>6 17:06<br>6 17:06   |
| external and peripheral) and intelligent and interface  (microcontroller and mux and internal and external and peripheral) and intelligent and interface  (microcontroller and mux and internal and external and peripheral) and structurable  (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface  (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core  (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core  (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  ((microcontroller and mux and internal and USPAT 2003/09/2)   | 6 17:05<br>6 17:05<br>6 17:06<br>6 17:06   |
| external and peripheral) and intelligent and interface  (microcontroller and mux and internal and external and peripheral) and intelligent and interface  (microcontroller and mux and internal and external and peripheral) and structurable  (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface  (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core  (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core  (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  ((microcontroller and mux and internal and USPAT 2003/09/2)   | 6 17:05<br>6 17:05<br>6 17:06<br>6 17:06   |
| interface (microcontroller and mux and internal and external and peripheral) and intelligent and interface  (microcontroller and mux and internal and external and peripheral) and structurable (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core  10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data) ((microcontroller and mux and internal and USPAT 2003/09/2)  2003/09/2)  | 6 17:05<br>6 17:06<br>6 17:06  |
| external and peripheral) and intelligent and interface  (microcontroller and mux and internal and external and peripheral) and structurable (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core  10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data) ((microcontroller and mux and internal and USPAT 2003/09/20)  2003/09/20  | 6 17:05<br>6 17:06<br>6 17:06  |
| external and peripheral) and intelligent and interface  (microcontroller and mux and internal and external and peripheral) and structurable (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core  10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data) ((microcontroller and mux and internal and USPAT 2003/09/20)  2003/09/20  | 6 17:05<br>6 17:06<br>6 17:06  |
| interface  (microcontroller and mux and internal and external and peripheral) and structurable  (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface  (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core  10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  ((microcontroller and mux and internal and USPAT 2003/09/20)  (microcontroller and mux and internal and interface and core and (process near data)  ((microcontroller and mux and internal and USPAT 2003/09/20)   | 6 17:06<br>6 17:06   |
| - 0 (microcontroller and mux and internal and external and peripheral) and structurable - 0 (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface - 44 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core - 10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data) - 7 ((microcontroller and mux and internal and USPAT 2003/09/2)  | 6 17:06<br>6 17:06   |
| external and peripheral) and structurable (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface  44 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core  10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  7 ((microcontroller and mux and internal and USPAT 2003/09/20)  | 6 17:06<br>6 17:06   |
| - 0 (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface - 44 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core - 10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data) - 7 ((microcontroller and mux and internal and USPAT 2003/09/20)   | 6 17:06  |
| external and peripheral) and "intelligent core" and interface  44 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core  10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  7 ((microcontroller and mux and internal and USPAT 2003/09/20)  | 6 17:06  |
| core" and interface  (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core  10 (microcontroller and mux and intelligent and external and peripheral) and intelligent and interface and core and (process near data)  ((microcontroller and mux and internal and USPAT 2003/09/20)  |  |
| - 44 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core - 10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data) - 7 ((microcontroller and mux and internal and USPAT 2003/09/20  |  |
| external and peripheral) and intelligent and interface and core  10 (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)  7 ((microcontroller and mux and internal and USPAT 2003/09/20)  |  |
| interface and core  (microcontroller and mux and internal and external and interface and core and (process near data)  ((microcontroller and mux and internal and USPAT 2003/09/20)  (microcontroller and mux and internal and USPAT 2003/09/20)   | 5 17:07  |
| - 10 (microcontroller and mux and internal and uspat 2003/09/20 external and peripheral) and intelligent and interface and core and (process near data) - 7 ((microcontroller and mux and internal and Uspat 2003/09/20  | 5 17:07  |
| external and peripheral) and intelligent and interface and core and (process near data)  7 ((microcontroller and mux and internal and USPAT 2003/09/20   | 5 17:07  |
| interface and core and (process near data) 7 ((microcontroller and mux and internal and USPAT 2003/09/20   |  |
| - 7 ((microcontroller and mux and internal and USPAT 2003/09/20  |  |
| - 7 ((microcontroller and mux and internal and USPAT 2003/09/20  |  |
|  | 5 17:08  |
| , , , , , , , , , , , , , , , , , , ,  |  |
| interface and core and (process near data))  |  |
| and circuit and dispose\$1   |  |
| - 7 ((microcontroller and mux and internal and USPAT 2003/09/20  | 5 17.09  |
| external and peripheral) and intelligent and   | , 17.05  |
| interface and core and (process near data))  |  |
| and circuit and dispose\$1 and multiplexer   |  |
|  |  |
| - 0 ((microcontroller and mux and internal and USPAT 2003/09/20  | 17:09  |
| external and peripheral) and intelligent and   |  |
| interface and core and (process near data))  |  |
| and circuit and dispose\$1 and multiplexer   |  |
| and configuration  |  |
| - 165 microcontroller and internal and external USPAT 2003/09/20   | 5 17:11  |
| and peripheral and circuit and dispose\$1 and  |  |
| multiplexer and configuration  |  |
| - 55 microcontroller and internal and external USPAT 2003/10/0   | € 17:50  |
| and peripheral and circuit and dispose\$1 and  |  |
| · multiplexer and configuration and array and  |  |
| NAND   |  |
| - 1 microcontroller and DNF USPAT 2003/09/20   | 5 17:12  |
| - 46 microcontroller and internal and external USPAT 2003/10/08  |  |
| and peripheral and circuit and dispose\$1 and  | , 10.23  |
| multiplexer and configuration and array and  |  |
| NAND and (state near machine)  |  |
|  |  |
| 11 microcontroller and internal and external USPAT 2003/09/20  | 5 17:19  |
| and peripheral and circuit and dispose\$1 and  |  |
| multiplexer and configuration and array and  |  |
| NAND and (state near machine) and fuse   |  |
| 9 microcontroller and internal and external USPAT 2003/10/08   | 3 18:29  |
| and peripheral and circuit and dispose\$1 and  |  |
| multiplexer and configuration and array and  |  |
| NAND and (state near machine) and fuse and   |  |
| reverse  |  |
| - 108 (712/ .*) and program-controlled USPAT 2003/10/08  | 18:49  |
| - 2 (712/ .*) and program-controlled and USPAT 2003/10/08  |  |
| (process near instruction\$1)  | , 10.50  |
|  | 10.55  |
| -   1   (712/ .*) and program-controlled and USPAT   2003/10/08   (process near instruction\$1) and internal   | , 10:21  |
|  |  |
| and peripheral   |  |

Search History 10/14/03 7:31:07 PM Page 1 C:\APPS\EAST\Workspaces\09816933\09816933.wsp

|   |       | ·   | ,                  |                  |
|---|-------|---|--------------------|------------------|
| - | 11    | (712/ .*) and program-controlled and intelligent                                | USPAT              | 2003/10/08 18:54 |
| - | 1     | (712/ .*) and program-controlled and  | USPAT              | 2003/10/08 18:52 |
| - | 2     | intelligent and peripheral (712/ .*) and program-controlled and                 | USPAT              | 2003/10/08 18:55 |
|   |       | intelligent and circuit and dispose\$1 and multiplexer and configuration        |                    |                  |
| - | 2     | (712/ .*) and program-controlled and intelligent and circuit and dispose\$1 and | USPAT              | 2003/10/08 18:55 |
| - | 2     | multiplexer and configuration 20020013630.pn.                                   | USPAT;             | 2003/10/09 17:53 |
|   |       | •   | US-PGPUB;          |                  |
|   |       |   | EPO; JPO; DERWENT: |                  |
|   |       |   | IBM_TDB            |                  |
| - | 56447 | ,   | USPAT              | 2003/10/14 14:18 |
|   | 37    | (712/ .*) and fuse and "anti-fuse"  | USPAT              | 2003/10/14 14:19 |